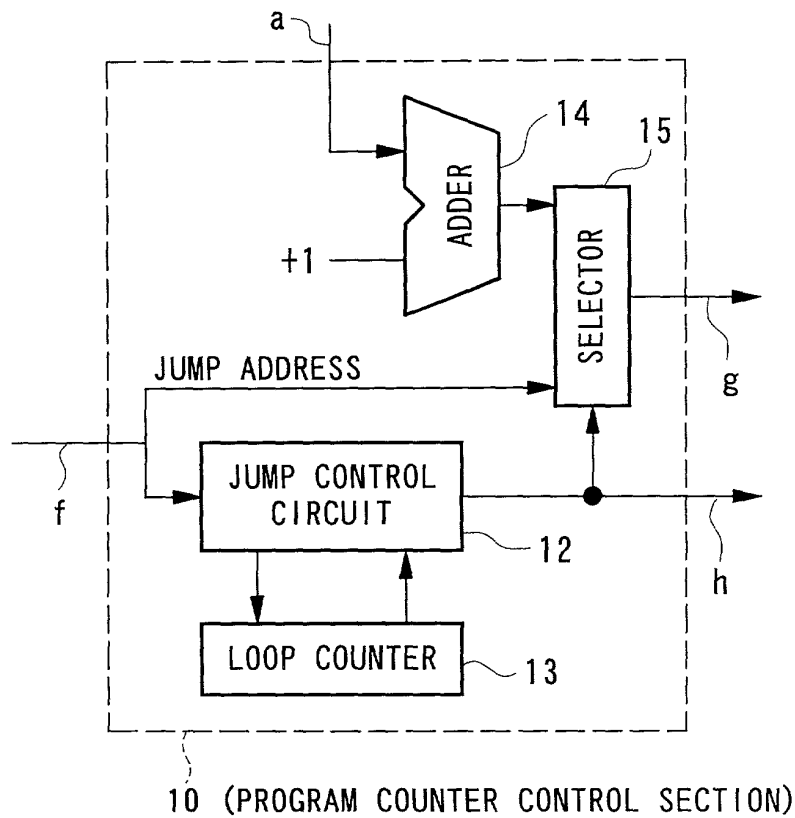




# FIG. 2



# FIG. 3

INSTRUCTION MEMORY ADDRESS	STORAGE AREA FOR SEQUENCE CONTROL INSTRUCTION ON NEXT LINE	STORAGE AREA FOR SEQUENCE CONTROL INSTRUCTION CONTAINED IN JUMP TARGET LINE
0	LOOP 3 AA	INDEFINITE
1	NOOP	LOOP 3 AA
2	⋮	INDEFINITE
⋮	⋮	⋮

# FIG. 4

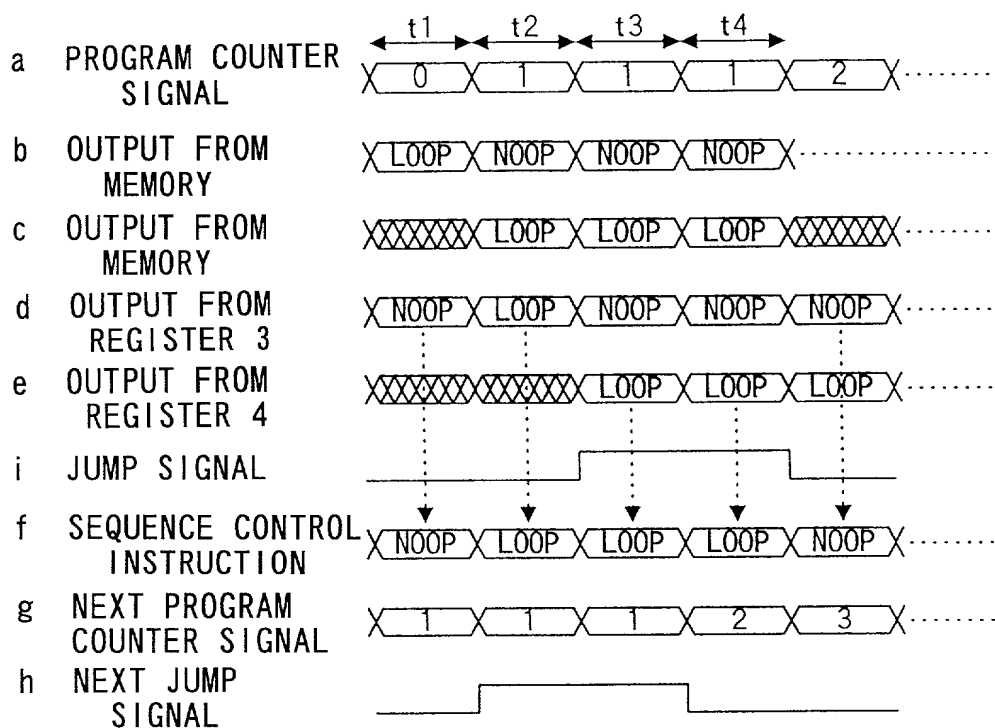


FIG. 5

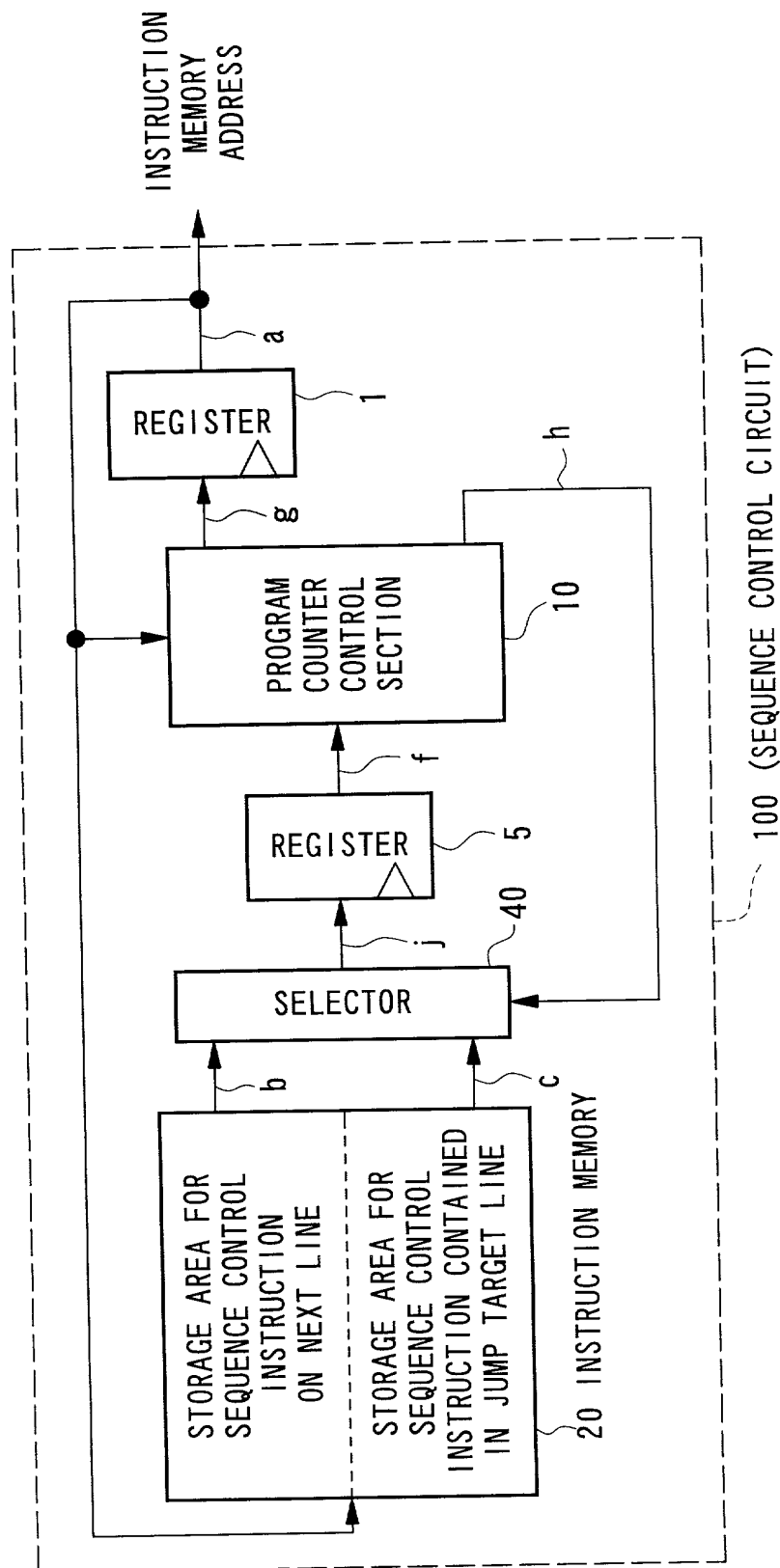
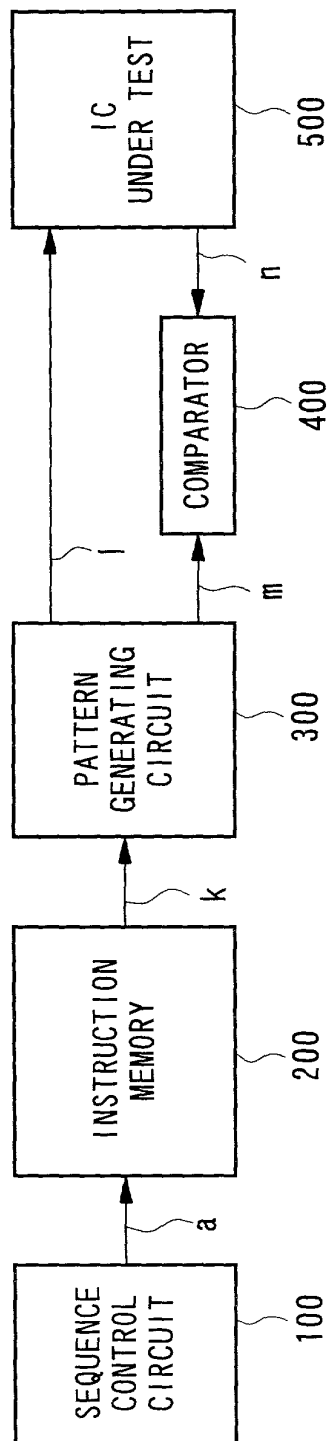


FIG. 6



# FIG. 7

PROGRAM COUNTER	SEQUENCE CONTROL INSTRUCTION	PATTERN GENERATING INSTRUCTION
0	NOOP	X=0
1	AA: LOOP 3 AA	X=X+1
2	NOOP	X=0
⋮	⋮	

# FIG. 8

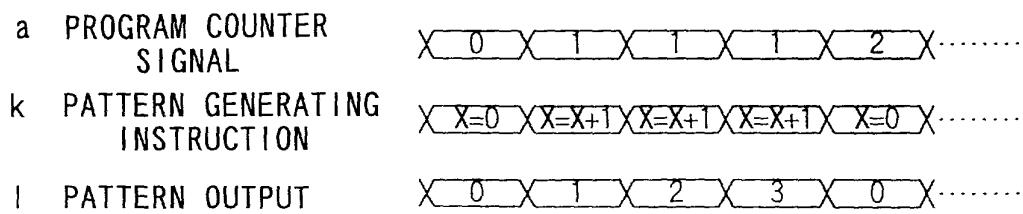


FIG. 9

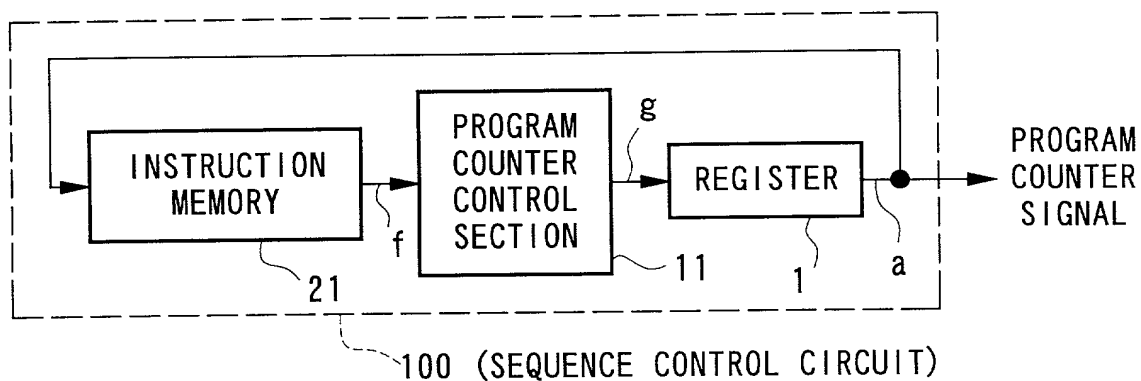


FIG. 10

